



Attorney Docket No. SEL 156 DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Shunpei YAMAZAKI et al

Serial No.: 09/874,670

Filed: June 5, 2001

Art Unit: 2825

Examiner: L. Malsawma

For: SEMICONDUCTOR DEVICE AND
MANUFACTURING METHOD THEREOF

I hereby certify that this correspondence
is being deposited with the United States
Postal Service as first class mail in an
envelope addressed to:
Commissioner for Patents, P.O. Box 1450,
Alexandria, VA 22313-1450, on 01/26/2004

Christine A. Barglik
Date: January 26, 2004
RECEIVED
FEB 2 2004
MAIL ROOM 2800

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, as revised on February 4, 1992, 1135 OG 23-24, Applicant hereby calls the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. Copies of the references are enclosed herewith for the Examiner's consideration.

No inference should be drawn that the attached list sets forth a comprehensive investigation of the prior art, that any or all are pertinent to the invention, or that any apparatus disclosed is equivalent to the subject invention.


The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not

possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

A first office action, notice of allowance or issue fee notification has been received in this case, so a check in the amount of \$180.00 is enclosed. If any additional fee is required, please charge Deposit Account No. 50/1039.

Respectfully submitted,


Mark J. Murphy
Registration No.: 34,225

COOK, ALEX, McFARRON, MANZO,
CUMMINGS & MEHLER, LTD.
200 West Adams Street
Suite 2850
Chicago, Illinois 60606
(312) 236-8500



LIST OF PUBLICATIONS CITED BY APPLICANT	<u>Attorney Docket No.</u> SEL 156 DIV	<u>Serial No.</u> 09/874,670
	<u>Applicant</u> Shunpei YAMAZAKI et al	
	<u>Filing Date</u> June 5, 2001	<u>Group</u> 2825

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	6,590,229 B1	07/08/03	Yamazaki et al	257	71	01/19/00

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE

OTHER PUBLICATIONS

(Including Author, Title, Date, Pertinent Pages)

1. TERADA et al, "Half-V Switching Mode FLCD," Proceedings of the 46th Applied Physics Association Lectures, 28p-V-8, p.1316, March, (1999), **with English abstract.**
2. YOSHIHARA, T. et al, "Time Division Full Color LCD by Ferroelectric Liquid Crystal," EKISHO, vol. 3, no. 3, pp. 190-194, (1999), **with English abstract.**

RECEIVED
FEB-4 2001
TECHNOLOGY CENTER 2800

EXAMINER:	DATE CONSIDERED:
-----------	------------------

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket SEL 156 DIV

Re Application of:

Shunpei YAMAZAKI et al

Serial No: 09/874,670

Filed: June 5, 2001

For: SEMICONDUCTOR DEVICE AND
MANUFACTURING METHOD THEREOF

I hereby certify that this correspondence
is being deposited with the United States
Postal Service as first class mail in an
envelope addressed to:
Commissioner for Patents, P.O. Box 1450,
Alexandria, VA 22313-1450, on 01/26/2004

Christine A. Barglik

Date: January 26, 2004

CORRECTION OF FORM 1449

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

On March 18, 2003 Applicant filed an information disclosure statement and PTO form 1449 which contained some typographical errors.

Specifically, under "Other Publications" on form 1449:

- Item 1 should include "vol. 6, no. 4,";
- Item 3 should include "pp. 782-785,";
- Item 4 should read "1988," (not "1998").

Accordingly, we are submitting a new 1449 form with this information. If any fee is required in connection with this matter, please charge Deposit Account no. 50/1039.

Respectfully submitted,

Mark J. Murphy
Mark J. Murphy
Registration No: 34,225

COOK, ALEX, McFARRON, MANZO,
CUMMINGS & MEHLER, Ltd.
200 West Adams Street, Suite 2850
Chicago, Illinois 60606 (312) 236-8500

RECEIVED
FEB - 4 2004
TECHNOLOGY CENTER 2800



LIST OF PUBLICATIONS CITED BY APPLICANT	<u>Attorney Docket No.</u> SEL 156 DIV	<u>Serial No.</u> 09/874,670
	<u>Applicant</u> Shunpei YAMAZAKI et al	
	<u>Filing Date</u> June 5, 2001	<u>Group</u> 2825

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE

OTHER PUBLICATIONS

(Including Author, Title, Date, Pertinent Pages)

1. INUI, S. et al, "Thresholdless Antiferroelectricity in Liquid Crystals and its Application to Displays," J. Mater. Chem., vol. 6, no. 4, pp. 671-673, (1996).
2. YOSHIDA, T. et al, "A Full-Color Thresholdless Antiferroelectric LCD Exhibiting Wide Viewing Angle with Fast Response Time," SID 97 Digest, pp. 841-844, (1997).
3. FURUE, H. et al, "Characteristics and Driving Scheme of Polymer-Stabilized Monostable FLCDC Exhibiting Fast Response Time and High Contrast Ratio with Gray-Scale Capability," SID 98 Digest, P-78, pp. 782-785, (1998).
4. SHIMOKAWA, R. et al, "Characterization of High-Efficiency Cast-Si Solar Cell Wafers by MBIC Measurement," Japanese Journal of Applied Physics, vol. 27, no. 5, pp. 751-758, May, (1988).
5. OHTANI, H. et al, "Late-News Poster: A 60-in HDTV Rear-Projector with Continuous-Grain-Silicon Technology," Society for Information Display, International Symposium Digest of Technical Papers, Anaheim, California, May 17-22, 1998, SID Digest, vol. XXIX, pp. 467-470, (1998).
6. TERADA et al, "Half-V Switching Mode FLCDC," Proceedings of the 46th Applied Physics Association Lectures, 28p-V-8, p.1316, March, (1999).
7. YOSHIHARA, T. et al, "Time Division Full Color LCD by Ferroelectric Liquid Crystal," EKISHO, vol. 3, no. 3, pp. 190-194, (1999).

EXAMINER:

DATE CONSIDERED: